L Number	Hits	Search Text	DB	Time stamp
35	19	("asynchronous packet header") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 07:41
36	38	("1394 header") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 07:41
37	3	("1394 header" with ("source id" "source identification")) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 07:43
38	3	("1394 header" same ("source id" "source identification")) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 07:44
39	19	("1394" with header same ("source id" "source identification")) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 08:18
40	81	(header same ((toggle change) with priority)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 08:29
41	26	(priority with bit with toggle) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 08:44
42	1341	("change priority") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 08:45
43	17	("change priority" with header) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 08:46
44	26	("change priority" same header) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 08:50
45	9	(("change priority" same header) and @ad<20010928) not (("change priority" with header) and @ad<20010928)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 08:46
46	290	(free with ((identification id) adj number)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 08:50
47	1801237	(free adj2h ((identification id) adj number)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 08:50
48	17	(free adj2 ((identification id) adj number)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 08:57

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49	2	(frees with ((identification id) adj number)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 08:57
50	468	((reuse re-use) with(identification id)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 08:58
51	40	((reuse re-use) with ((identification id) adj number)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 09:22
52	6	("correlative id number") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 09:22
53	1	("correlative identification number") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 09:41
54	7	(("correlative id number") and @ad<20010928) (("correlative identification number") and @ad<20010928)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 09:22
55	0	(("cycle identification" "cycle id") with "toggle bit") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 09:41
56	0	(("cycle identification" "cycle id") same "toggle bit") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 09:41
57	0	(("cycle identification" "cycle id") same "toggle-bit") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 09:41
60	11	((multi-processor multiprocessor "multiple processors") same ("processor buses" "processor busses") same (controller controllers)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 10:40
61	1		USPAT	2004/03/17 10:09
62	1		USPAT	2004/03/17 10:09
63	9	((multi-processor multiprocessor "multiple processors") same ("multiple buses" "multiple busses") same (controller controllers)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 10:33
64	44	((multi-processor multiprocessor "multiple processors") same ("processor buses" "processor busses")) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 10:40
65	33	(((multi-processor multiprocessor "multiple processors") same ("processor buses" "processor busses")) and @ad<20010928) not (((multi-processor multiprocessor "multiple processors") same ("processor buses" "processor busses") same (controller controllers)) and @ad<20010928)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 10:40

66	1		USPAT	2004/03/17 10:49
67	1		USPAT	2004/03/17 10:49
68	1		USPAT	2004/03/17 10:49
69	1		USPAT	2004/03/17 10:50
70	1		USPAT	2004/03/17 10:50
71	1		USPAT	2004/03/17 10:50
-	874	(710/305.ccls.) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/15 15:13
-	0	("memory controller" and "processor controller" and "coherency controller") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/15 15:14
-	54	("memory controller" and "processor controller" and "cache controller") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/15 15:29
-	1548	("memory controller" and ("cache controller" "coherency controller")) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/15 15:29
-	1	("host controller" same ("memory controller" same ("cache controller" "coherency controller"))) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/15 15:42
-	1		USPAT	2004/03/15 15:41
-	2	("host controller" same ("main memory" same ("cache memory"))) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/15 15:49
-	1037	(bridge same ("main memory" same ("cache memory"))) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/15 15:50
-	214	(bridge with ("main memory" with ("cache memory"))) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 07:43
-	2	6098132.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 07:04
-	733	("unidirectional bus" "uni-directional bus") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 07:43
-	179	(("unidirectional bus" "uni-directional bus") same processor) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 07:43

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–	26	(("unidirectional bus" "uni-directional bus") same processor same cache) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 07:49
–	2	(("unidirectional bus" "uni-directional bus") with ("well known" well-known)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 07:54
–	27	(("unidirectional bus" "uni-directional bus") with local) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:28
–	3	6601126.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 07:56
–	247	(("point to point bus" "point-to-point bus")) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:31
–	0	(("point to point bus" "point-to-point bus") with ("well known" well-known)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 12:46
–	139	(("point to point" "point-to-point") with ("well known" well-known)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 11:23
–	28	kosaraju.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:52
–	1	("single transaction" with "request operation") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 11:25
–	1	("single transaction" same "request operation") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 12:44
–	890	((processor cpu) with "internal cache") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 12:45
–	642	((processor) with "internal cache") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 12:45
–	8	("internal cache" with ("well known" well-known)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 12:49
–	12	("internal cache" with (improved)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 12:59

-	14	("internal cache" with (efficient efficiency)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 12:52
-	22	("processor cache" with (improved)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 13:09
-	650	((multi-processor multiprocessor "multiple processors") same ("processor buses" "processor busses")) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 13:10
-	0	((multi-processor multiprocessor "multiple processors") same ("processor buses" "processor busses") same "not coupled") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 13:11
-	0	((multi-processor multiprocessor "multiple processors") same ("processor buses" "processor busses") same "not connected") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 13:11
-	0	((multi-processor multiprocessor "multiple processors") same ("processor buses" "processor busses") same "processor controllers") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 13:11
-	157	((multi-processor multiprocessor "multiple processors") same ("processor buses" "processor busses") same controllers) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 10:06
-	85	((multi-processor multiprocessor "multiple processors") same ("processor buses" "processor busses") same bridge) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 13:39
-	5	((multi-processor multiprocessor "multiple processors") same ("processor buses" "processor busses") same bridge) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 13:43
-	591	(plurality with "processor buses") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 13:43
-	0	("plurality of processor buses") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 13:44
-	92	(plurality adj3 "processor buses") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 13:44
-	56	(plurality adj2 "processor buses") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 14:37
-	44	(source with destination with cycle with (id identification)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 14:56

-	0	((cycle adj (id identification)) same (source adj (id identification))) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 14:51
-	268	(cycle adj (id identification)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 14:52
-	127	(source with destination with transaction with (id identification)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 15:17
-	33	(source with destination with (id identification) with known) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 15:34
-	64	(signal with header with ("well known" well-known)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 15:44
-	433	(signal with header with bus) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 15:44
-	404	(signal with header with bus not usb not "1394" not firewire) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 15:52
-	0	("aysynchronous packet" same "internal bus") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 15:53
-	0	("aysynchronous packet") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 15:52
-	2	("asynchronous packet" same "internal bus") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 15:57
-	658	("asynchronous packet") and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 07:19
-	9	("asynchronous packet" with $5known) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 16:04
-	611	(signal with ((id identification) adj tag)) and @ad<20010928	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 16:08
-	302	((signal with ((id identification) adj tag)) and @ad<20010928) not ("radio frequency" rf)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 16:09

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